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A wafer, also called a slice or substrate, is a thin slice of semiconductor material, such as a crystalline silicon, used in electronics for the fabrication of integrated circuits and in photovoltaics for conventional, wafer-based solar cells. The wafer serves as the substrate for microelectronic devices built in and over the wafer and undergoes many microfabrication process steps such as ...

### Wafer (electronics) - Wikipedia

3-LAYER POLYSILICON SURFACE MICROMACHINING PROCESS 1 PolyMUMPs Design Handbook, Rev. 13.0 Chapter 1 Three-Layer Polysilicon Surface Micromachining Process 1.1 Introduction The Multi-User MEMS Processes, or MUMPs®, is a commercial program that provides cost-effective, proof-of- concept MEMS fabrication to industry, universities, and government worldwide.

#### PolyMUMPs Design Handbook - MEMSCAP

In microelectronics, a three-dimensional integrated circuit (3D IC) is an integrated circuit manufactured by stacking silicon wafers or dies and interconnecting them vertically using, for instance, through-silicon vias (TSVs) or Cu-Cu connections, so that they behave as a single device to achieve performance improvements at reduced power and smaller footprint than conventional two dimensional ...

# Three-dimensional integrated circuit - Wikipedia

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3.4. Delamination. A delamination condition occurs when a material interface loses its adhesive bond. It can be induced by a number of means, from mask misalignments to particulates on the wafer during processing.

#### Journal of Quality and Reliability Engineering - Hindawi

In general, polymers with a R 2 Si O unit are termed silicones, while the Si O repeat unit is also called siloxane. The strength of the Si O bond gives the polymer its thermal and chemical stability, which is important for its use in high-temperature applications [1,2]. In PDMS, the flexibility of the siloxane backbone permits the chains to easily arrange and rearrange themselves so as to place ...

# PDMS with designer functionalitiesâ€"Properties

Specimen manipulation strategies are illustrated schematically across the length scales involved in microand nanotensile testing. The top row shows examples of specimens that can be handled using tweezers. The middle and bottom rows require manipulators to harvest and transfer specimens to a ...

#### The Micro- and Nanoscale Tensile Testing of Materials

Legenda S5C2 S5C1 S4C2 S4C1 S3C2 S3C1 S2 S1C2 S1C1 aMinisterSectiunea4cucalculBalaci Railway applications - Fixed installations - Particular requirements for a.c ...

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